

“ “ ” “ ”
“ 2017 ” 2024
“ 2017 2024
” “ ”

	17 MTN001
	2017 2024
	2024 8 14 10:00

10

“ 17

MTN001 "

100%

5+N

5+3

17

MTN001

7

10

100%

17

MTN001

0

0

0%

17

MTN001

0

0

0%

100%

Diagram illustrating a sequence of operations on a memory stack (addresses 0 to 10):

- Initial state: Stack contains 17 at address 0, MTN001 at address 1, and 7 at address 2.
- Operation 1: Push (adds 0 at address 3).
- Operation 2: Pop (removes 0 at address 3).
- Final state: Stack contains 17 at address 0, MTN001 at address 1, and 7 at address 2.

17	MTN001	7	10
	100%	17	MTN001
0	0		

50BP



